

# CS 631-02 Processor Design Control

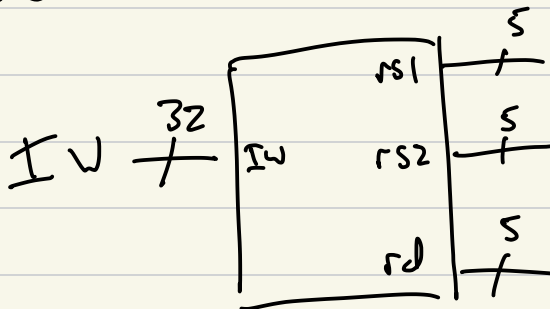
## Decoders

Reg Decoder

Imm Decoder

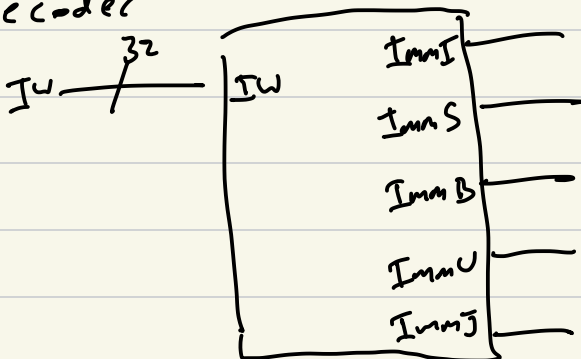
Inst Decoder

## Reg Decoder

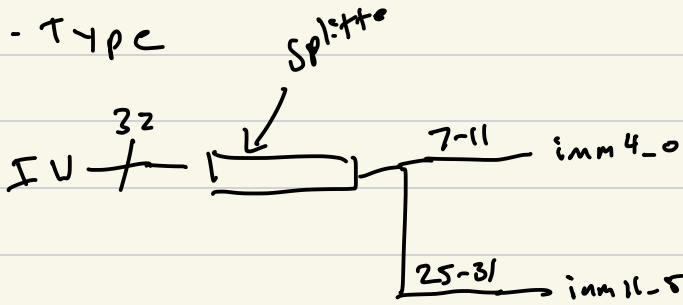


one splitter

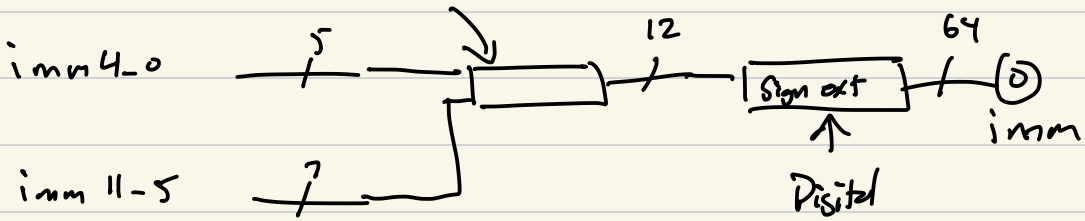
## Imm Decoder



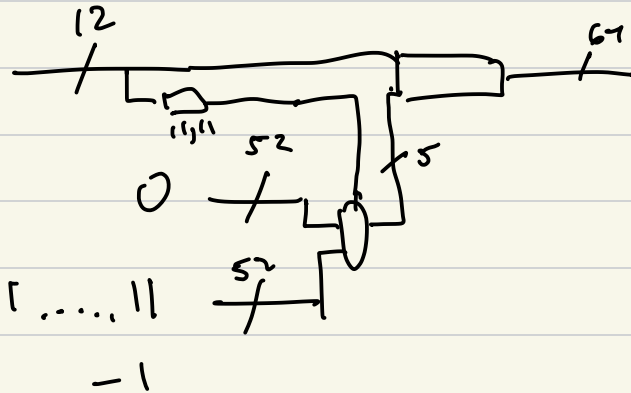
S-Type



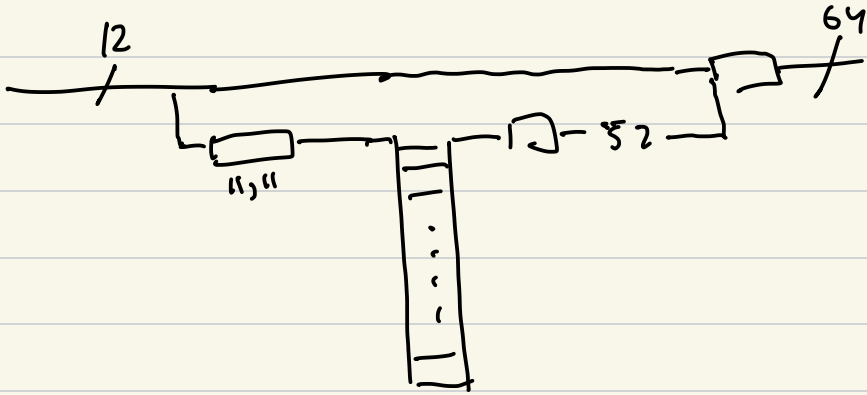
Goal: 64 bit sign extended value



MUX Approach



# Splitter approach



# SUB approach

